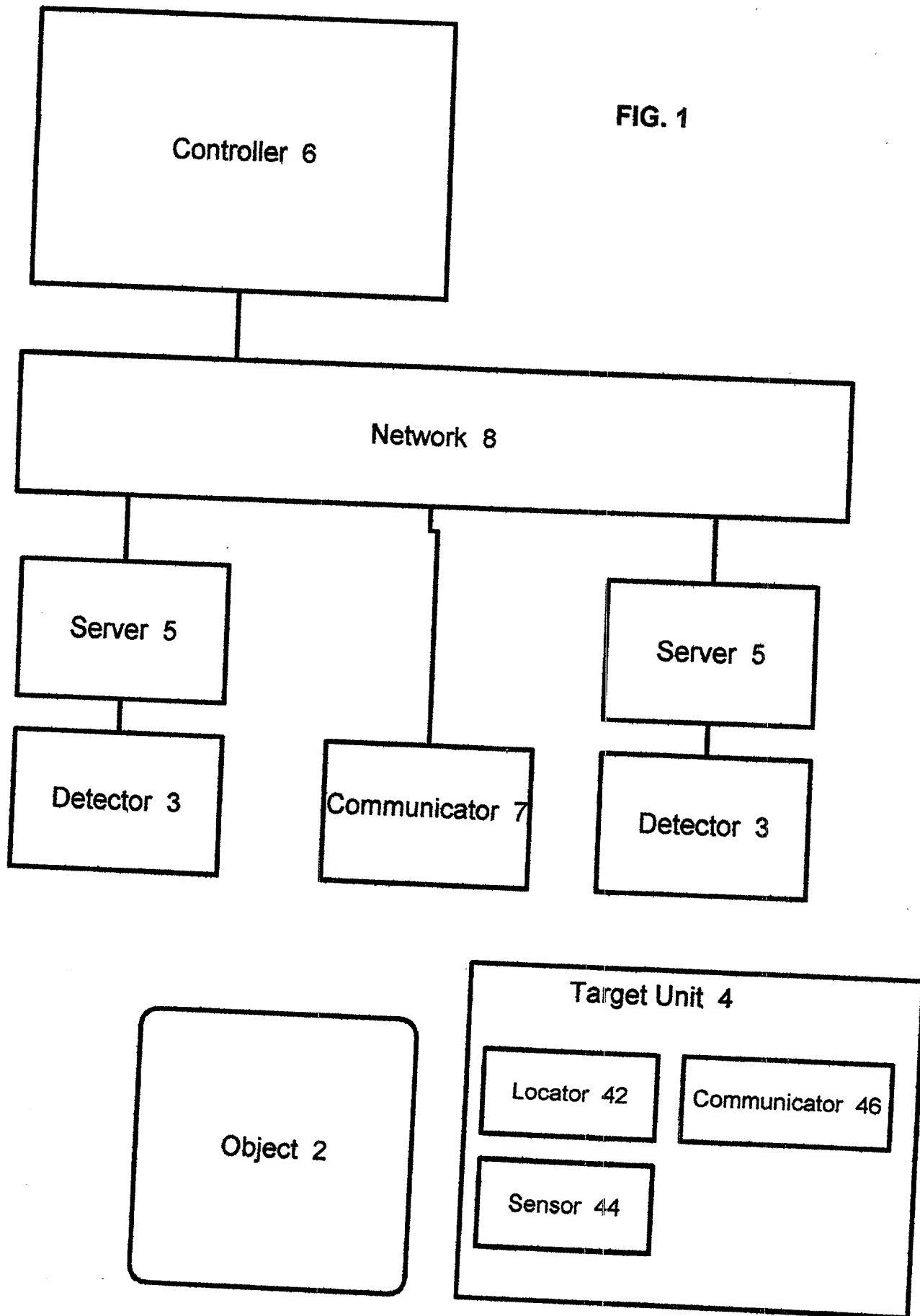


FIG. 1



Target Unit 4

Output 43

Input 44

Locator 42

Communicator 46

Processor 48

Storage 49

FIG. 2

FIG. 3

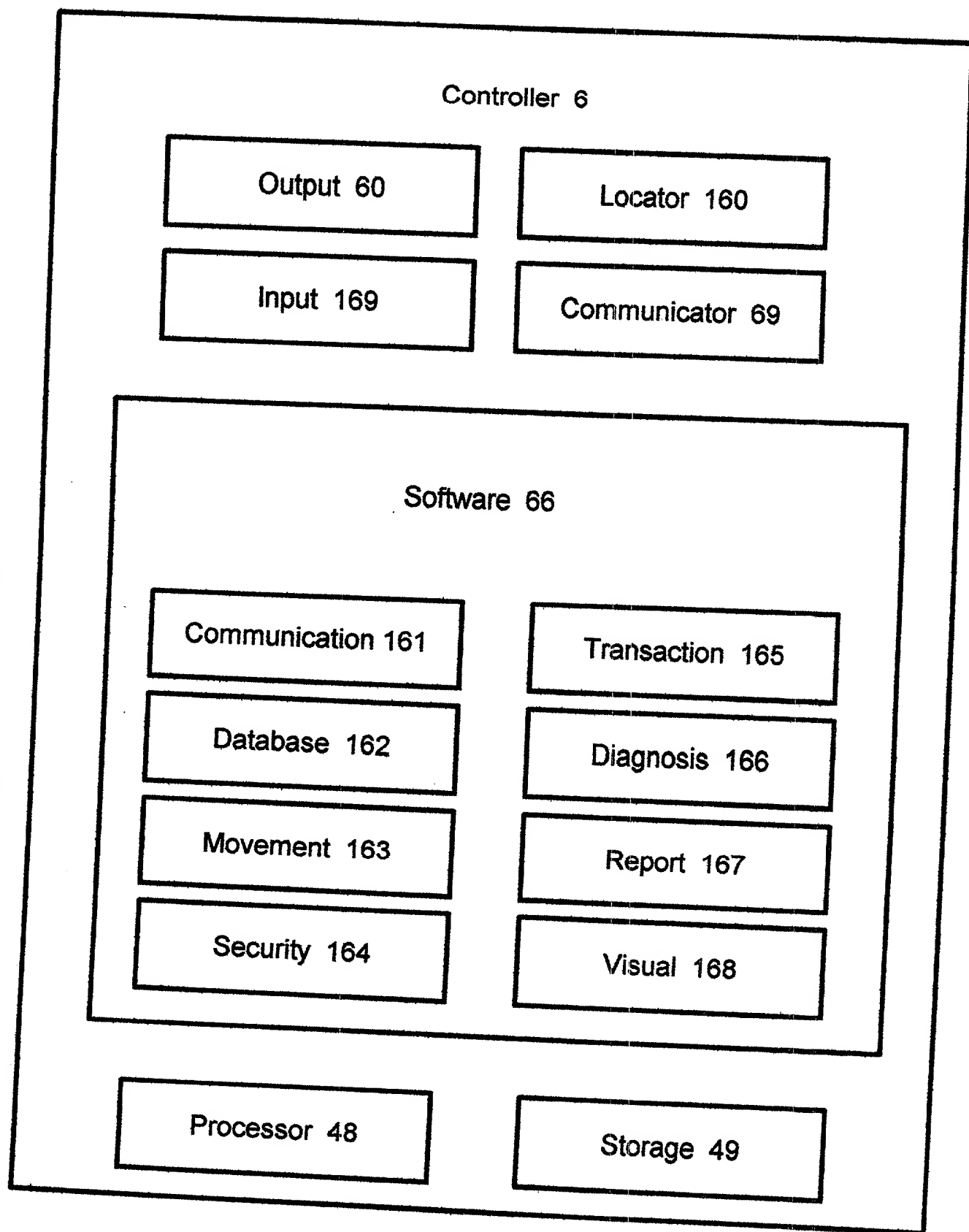


FIG. 3

FIG. 4

